## 32.768kHz CERAMIC SMD CRYSTAL OSCILLATOR



5.0 x 3.2 x 1.3mm

#### **FEATURES**:

• Pb-Free/RoHS Compliant

**ASFLK SERIES** 

- Tri state function
- IR reflow capable
- Seam sealed ceramic
- Suitable for high density SMT

**STANDARD SPECIFICATIONS:** 

- package

- > APPLICATIONS:
  - CCD clock for VTR camera

Pb

- Computers and peripherals
- Portable MP3 players and games

RoHS

Compliant

ABRACON P/N:	ASFLK Series
Frequency range:	32.768kHz
Dperating temperature:	0°C to + 70°C (see options)
Storage temperature:	-55°C to +125°C
Overall Frequency Stablilty:	± 100ppm max. (see options)
Supply Voltage (Vdd):	3.3 Vdc ± 10% (see options)
Supply current:	Vdd = 2.5 ~ 3.3V, 0.5mA typ. 1.0mA max.
	Vdd = 5.0V, 1.5mA typ. 5.0mA max.
Symmetry:	40/60 % max.@ 1/2Vdd (see options)
Rise and fall time (Tr/Tf):	250 ns max.
Dutput Load:	15pF or 10TTL max.
Dutput Voltage:	VOH = 2.97V min. VOL = 0.33V max.
Start-up-time	30ms max.
Tristate function:	Logic "1" or open: Oscillation
	Logic  "0" (VIL < 0.8 Vdc) : Hi Z
Aging at 25°C/year :	± 3ppm/year max.
Phase jitter:	1ps RMS max.

#### **OPTIONS AND PART IDENTIFICATION:**

(Left blank if standard)

¥		
Supply Voltage		
Blank	3.3V±10%	
1	3.0V±10%	
2	2.5V±10%	
4	5.0V±10%	

-		$\backslash$
Operating Temp.		
E	-20°C to +70°C	
F	-30°C to +70°C	
N	-30°C to +85°C	
L	-40°C to +85°C	
*Contact ABRACON for extended.		
temperature range		

ASFLK- <u>-</u> - 32.768kHz - <u>-</u> - <u>-</u> - <u>-</u> - <u>-</u>

nperature range

•		
Freq. Stability		
J	±20ppm	
R	±25ppm	
K	±30ppm	
Н	±35ppm	
В	±40ppm	
С	±50ppm	



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Packaging

Symmetry

Т

S

Tape and

Reel

45/55% max

at 1/2Vdd

### 32.768kHz CERAMIC SMD CRYSTAL OSCILLATOR



ASFLK SERIES

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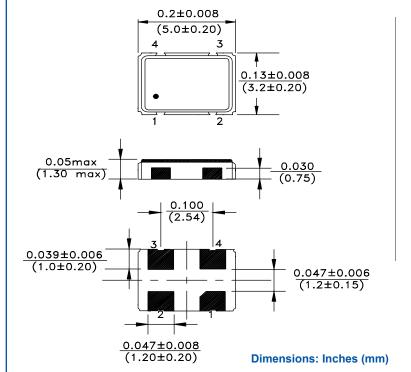
RoHS Compliant

Recommended Land Pattern

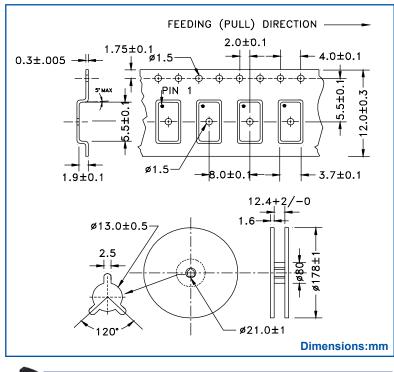
 $0.100 \pm 0.006$ 

5.0 x 3.2 x 1.3mm

 $\frac{0.087\pm0.006}{(2.2\pm0.15)}$ 



### **TAPE & REEL:** Tape and reel (1,000pcs/reel)



Need a test socket for the ASFLK series? To view compatible **PRECISION TEST & BURN-IN SOCKETS** for these parts, **click here.** 

ABRACON IS ISO 9001 / QS 9000 CERTIFIED



# PIN FUNCTION end using y 0.01uF 1 Tri-state z GND 3 output 3 Output

Vdd

0.067

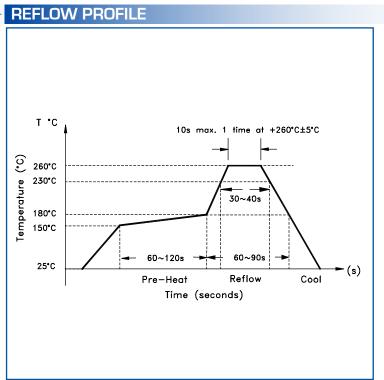
(1.7)

an approximately 0.01uF bypass capacitor between PIN 2 and 4.

Note: Recommend using

0.06

(1.5)



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\* **NOTE:** Abracon manufactured products are intended for general commercial and industrial use. For applications requiring high reliability and/or presenting extreme operating environment, written consent & authorization from Abracon is required.

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